

Cryogenic Performance of a Low-Noise JFET-CMOS Preamplifier for HPGe Detectors

Alberto Pullia, Francesca Zocca, Stefano Riboldi, Dusan Budjáš, Alessio D'Andragora, and Carla Cattadori

Abstract—Cryogenic low-noise charge sensitive preamplifiers have been developed and realized for the GERmanium Detector Array (GERDA). An integrated JFET-CMOS preamplifier, which is fully functional at cryogenic temperatures, has been tested in conjunction with an unsegmented p-type HPGe detector. Both the crystal and the preamplifier were operated inside a liquid nitrogen dewar at 77 K. The detector capacitance was ~ 60 pF. An optimum resolution of 1.6 keV FWHM has been obtained for the pulser line at 6 μ s shaping time. A resolution of 2.1 keV FWHM has been achieved for the 1.332 MeV line from a ^{60}Co source. A wide bandwidth (rise time of ~ 16 ns) permits use of pulse-shape analysis techniques to localize the position of the photon interactions inside the crystal. A low power consumption (~ 23 mW) makes the preamplifier suitable for a multi-channel array of germanium detectors.

Index Terms—CMOS preamplifier, cryogenic low-noise preamplifier, gamma-ray spectroscopy, germanium detectors.

I. INTRODUCTION

USE of High-Purity Germanium (HPGe) detectors is foreseen in the GERDA experiment (GERmanium Detector Array) [1], [2] in the search of neutrino-less double-beta decay of ^{76}Ge . Bare germanium detectors, isotopically enriched in ^{76}Ge , are operated in liquid argon. The cryogenic liquid is used both as a cooling medium and as a shield against external radiation. The charge preamplifiers must operate as completely immersed in the cryogenic liquid. Being an ultra-low background radiation detection system, the front-end electronics must also provide a high degree of radio-purity. Some different solutions have been studied and developed for the read-out of the GERDA crystals [3]–[5]. In particular a JFET-CMOS low-noise charge preamplifier has been developed, realized and tested. In this work the very good performances obtained are described. The circuit has been tested when coupled to an unsegmented coaxial HPGe crystal. Both the detector and the preamplifier were immersed in a liquid nitrogen dewar and operated at 77 K. The

preamplifier performances meet the needed challenging requirements of functionality at cryogenic temperatures, low noise, low power consumption, rise time of < 20 ns and high closed-loop gain stability.

II. DETECTOR SETUP AND CHARGE-PREAMPLIFIER STRUCTURE

In Fig. 1 the cryogenic detector setup developed at Max-Planck-Institut fuer Kernphysik (MPIK) in Heidelberg [1] is shown. An unsegmented p-type coaxial closed-end HPGe crystal is encapsulated in a standard silicon holder and operated in a vacuum chamber (Fig. 1(a)). The crystal has a diameter of 5.2 cm and an height of 5.1 cm. A plate for the mounting of the front-end electronics is placed below the vacuum chamber at a distance of ~ 5 cm (Fig. 1(a)). Both the chamber and the plate are supported by a steel frame (Fig. 1(b)) so as to be directly immersed in a liquid nitrogen dewar (Fig. 1(c)). The germanium crystal is thermally connected to the cryostat (Fig. 1(a)) and therefore operated at 77 K as well as the front-end electronics. The detector outer contact is biased at the high voltage of 2.5 kV. The inner contact is the read-out electrode and is DC-coupled to the preamplifier. As the read-out electrode receives holes, the signal polarity at the preamplifier output is negative.

In Figs. 2 and 3 the realized charge-sensitive preamplifier is shown. It is optimized for negative output signals and has the circuit structure shown in Fig. 2. It consists of an external low-noise silicon JFET (Junction Field Effect Transistor) manufactured by Philips, mod. BF862, an external feedback network ($C_F = 0.2$ pF, $R_F = 1.2$ G Ω), and an ASIC (Application Specific Integrated Circuit) used as low-noise operational amplifier along the negative-feedback loop. The ASIC has been realized in a 5 V 0.8 μm silicon CMOS technology. The discrete input devices as well as the integrated circuit are mounted on a PCB of 0.8 mm PTFE (teflon) laminate, as shown in Fig. 3.

The design of an integrated preamplifier for high-resolution gamma-ray spectroscopy is particularly challenging. A large dynamic range of at least 70 dB is required, while in contrast the available voltage swing of scaled CMOS technologies is lower and lower. Moreover, a long-term gain stability better than 0.2% and a high integral linearity over the full range are required. The output stage is a very critical part as the signal is to be transmitted to a remote receiver through a long terminated coaxial cable. The output stage has to provide the peak power in the shortest possible time and it should be able to provide a rail-to-rail output voltage swing. A standard high output impedance solution is not particularly adequate for the present application as it makes the preamplifier loop gain highly dependent on the value of the output load resistor. A low output impedance

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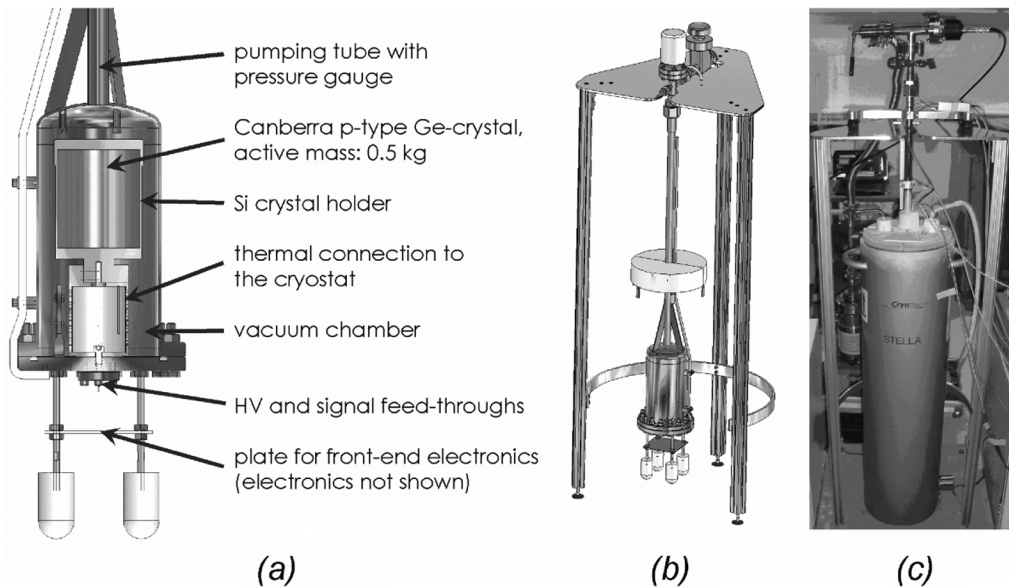


Fig. 1. Detector cryogenic set-up. (a) Vacuum chamber containing the encapsulated HPGe crystal. A plate for the mounting of the front-end electronics is placed below the chamber. (b) Steel frame supporting the vacuum chamber and the charge preamplifiers. (c) Liquid nitrogen dewar where the vacuum chamber and the charge preamplifiers are immersed and operated at 77 K.

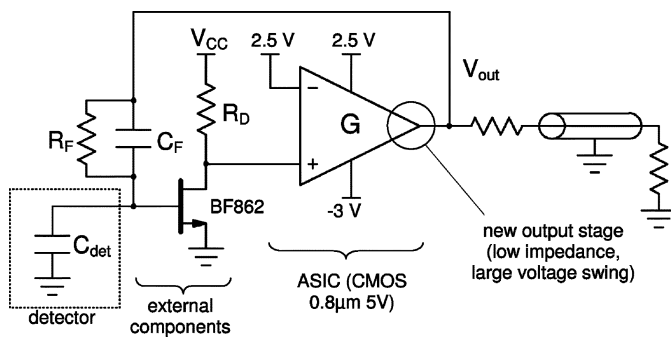


Fig. 2. Charge sensitive preamplifier structure, consisting of an external JFET, an external feedback network, and an ASIC used as low-noise operational amplifier along the negative-feedback loop.

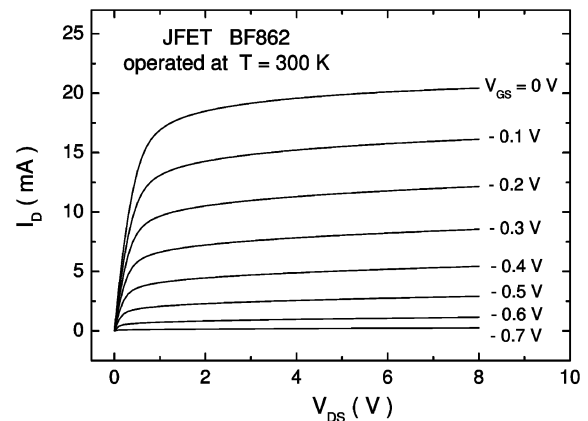


Fig. 4. Characteristic curves of the input Si-JFET BF862 as measured at room temperature.

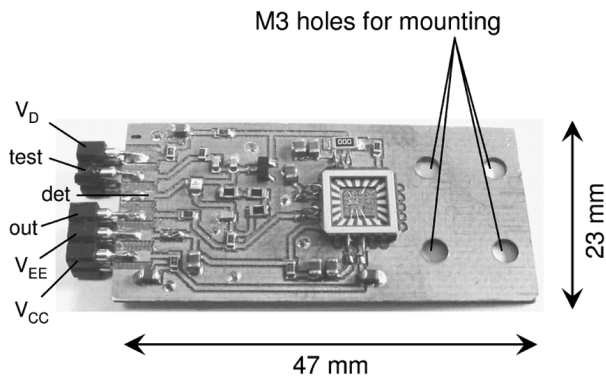


Fig. 3. Picture of the realized JFET-CMOS preamplifier as mounted on a PCB of 0.8 mm teflon laminate. The ASIC is realized in a 5 V 0.8 μm silicon CMOS technology.

resolution, optimized for negative signals (holes) has been realized, consisting of a self-adjusting constant current NMOS

source-follower. The current load is provided by a NMOS transistor acting as a driver. The detailed structure of the CMOS amplifier is described in [6], [7].

III. TEST-BENCH CHARACTERIZATION OF THE PREAMPLIFIER AT $T = 300\text{ K}$ AND $T = 77\text{ K}$

The preamplifier has been test-bench characterized, by simulating the detector with a capacitance and by injecting fast test pulses at the input node through a 1 pF test capacitance. The characteristic curves of the input BF862 Si-JFET have been measured both at room temperature (300 K) and in liquid nitrogen (77 K). The results are shown in Figs. 4 and 5. The characteristic curves of the input JFET substantially change when the temperature is decreased from 300 K to 77 K, as the freeze-out phenomena show up [8]. For given values of the gate-to-source and gate-to-drain voltages, the drain current decreases by a factor of ~ 5 . The changed bias point at cryogenic temperature yields a remarkable loss of device performance, but

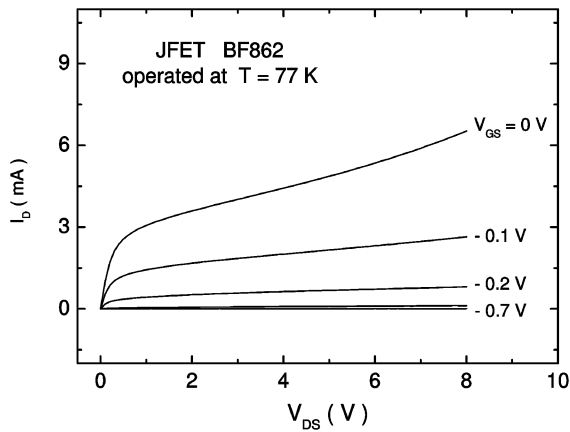


Fig. 5. Characteristic curves of the input JFET BF862 as measured at 77 K, when the device is directly immersed in liquid nitrogen. The drain current decreases by a factor of ~ 5 , yielding a substantial drop in the transconductance value.

the related drop of static power consumption is to be regarded as an advantage in this context. At room temperature the power supply voltage used for the external JFET is 12 V and the drain current is 14 mA, so that the power consumption is 168 mW. At 77 K the power supply voltage is 4 V and the drain current is 3 mA, so that the power consumption is only 12 mW. This reduces the liquid nitrogen micro-boiling phenomena as well as the related issues of microphonism; the cryogenic liquid consumption rate per unit preamplifier gets also substantially reduced. On the other hand, the changed JFET bias point determines a drop in the JFET transconductance, which can affect noise and bandwidth performances.

As far as noise is concerned, the Equivalent Noise Charge (ENC) of the circuit has been measured as a function of the shaping time, while simulating the detector with a capacitance of 15 pF, as shown in Fig. 6. The decreased temperature only partially compensates the effect of a decreased transconductance, so that the overall white noise is higher at 77 K than at room temperature. The optimum operating point for the input JFET would be at 120 K, where the transconductance reaches the maximum value and the noise is at the minimum level [9], [10]. Unfortunately the front-end devices will not be operated at 120 K in the GERDA apparatus, which is the framework of this electronic development. Bare germanium detectors will be operated in a liquid argon cryostat having linear dimensions of several meters. The charge preamplifiers will be directly immersed in liquid argon at 86 K. To let them operate at a higher temperature, a sealed vacuum box warming-up the front-end devices would be needed inside the cryostat, but the extremely low background radioactivity which is required in the proximity of the detectors does not allow for such a solution. Moving the front-end devices at a safer distance for radio-purity issues is not acceptable as well, because the long path from the detector electrodes to the input transistor would cause a substantial increase of the electronic noise and of pick-up disturbances and hence would completely spoil the spectroscopic performances. The choice of using all the same a Si-JFET as input transistor is dictated by the extremely low level of $1/f$ -like noise of such devices, which is particularly important for the present application. At a

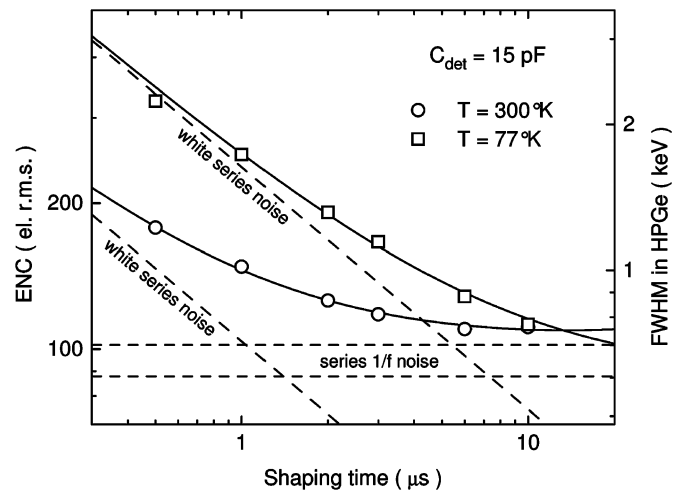


Fig. 6. Noise of the preamplifier as measured at the test bench, both at 300 K and 77 K, as a function of the shaping time. The detector capacitance is 15 pF.

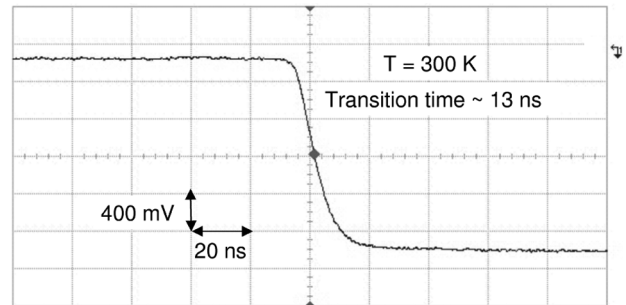


Fig. 7. Scope trace of the preamplifier response to a fast input pulse at 300 K. The transition time is as fast as 13 ns. The detector capacitance is 15 pF. The Miller compensation capacitance inside the ASIC is 0.6 pF.

shaping time of 10 μ s, the ENC achieves the same value both at room and at cryogenic temperature, or ~ 112 electrons r.m.s. (corresponding to 0.77 keV FWHM in HPGe). Hence, the noise performance remains fully compatible with gamma-ray spectroscopy requirements. The drop in the JFET transconductance at 77 K may be expected to determine a drop in the preamplifier bandwidth too. The opposite result is instead found: the preamplifier bandwidth increases when the circuit is operated at cryogenic temperature. The reason is easily found in the behavior of the transconductance of MOSFET transistors when operated at 77 K. The MOSFET's transconductance definitely increases while temperature decreases. At low channel fields it is proportional to the low-field carrier mobility, which increases by a factor of 4 to 6 while decreasing the temperature from 300 to 77 K, because of the reduced carrier scattering due to lattice vibration [11]. The higher gain of the ASIC amplifier at 77 K determines a higher loop gain of the overall preamplifier and a consequent wider bandwidth. While injecting a fast test pulse at the preamplifier input, a transition time of 13 ns is measured for the preamplifier response at room temperature, as shown in Fig. 7. When the circuit is operated in liquid nitrogen, the measured transition time is much faster but the output response shows some ringing. In order to get a fast but stable circuit response at cryogenic temperature, the preamplifier bandwidth has to be

TABLE I
TRANSITION TIMES MEASURED AT THE TEST BECH AS FUNCTION OF THE
DETECTOR CAPACITANCE (ASIC MILLER CAPACITANCE OF 1 pF)

C_{DET}	Transition time @ T = 300 K	Transition time @ T = 77 K
0 pF	14 ns	7 ns (ringing)
22 pF	21 ns	12 ns (overshoot)
33 pF	26 ns	13 ns
56 pF	34 ns	18 ns
100 pF	50 ns	28 ns

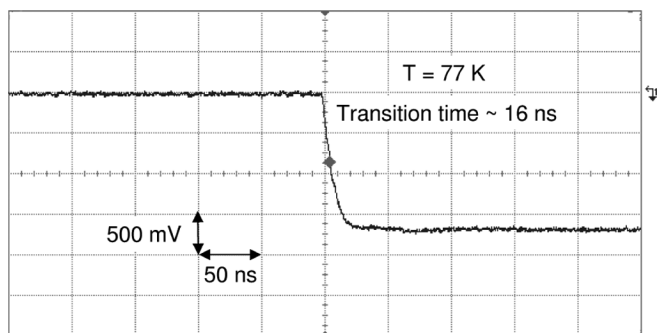


Fig. 8. Scope trace of the preamplifier output response to a fast input test pulse when the circuit is connected to the detector and operated at 77 K. The transition time is as fast as 16 ns.

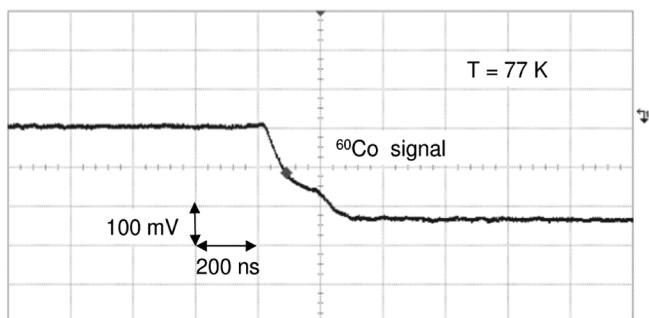


Fig. 9. Scope trace of a signal from a ^{60}Co radioactive source. The shape of the leading edge is well identified and permits application of pulse shape analysis algorithms.

reduced by increasing the value of the Miller compensation capacitance inside the ASIC amplifier, from a value of 0.6 pF to 1 pF. In Table I the measured transition time values are shown as obtained at 300 K and 77 K with a 1 pF Miller capacitance. Note that the preamplifier bandwidth has been optimized in order to get a fast and stable response at 77 K with the highest expected values of the detector capacitance, at the expenses of a decreased bandwidth at room temperature.

IV. EXPERIMENTAL MEASUREMENTS WITH THE DETECTOR SETUP: PREAMPLIFIER PERFORMANCES AND ENERGY RESOLUTIONS

The preamplifier has then been tested with the detector experimental setup described in Section II (see Fig. 1). In Figs. 8–12 some of the very good results obtained are shown.

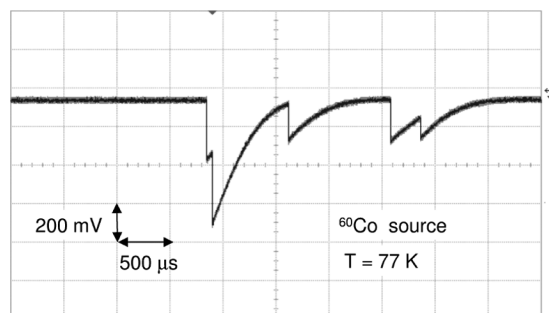


Fig. 10. Scope trace of a preamplifier output signal in the presence of a ^{60}Co source. The time constant of the baseline recovery is about 250 μs .

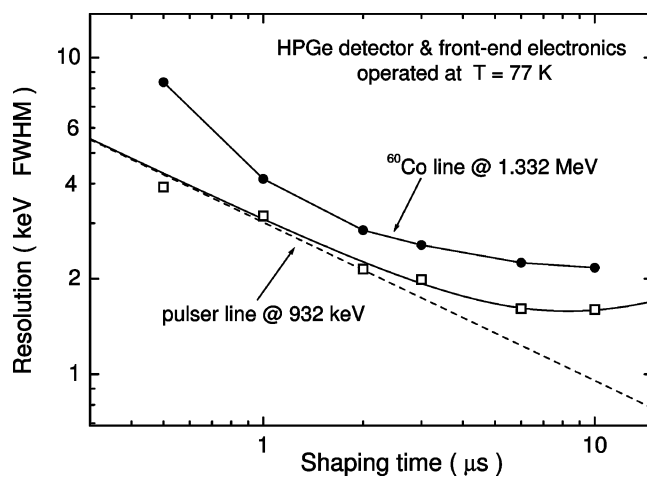


Fig. 11. Energy resolution as a function of the shaping time. The optimum resolution of 1.6 keV FWHM is achieved for the pulser line at 6 μs shaping time. The detector capacitance is ~ 60 pF.

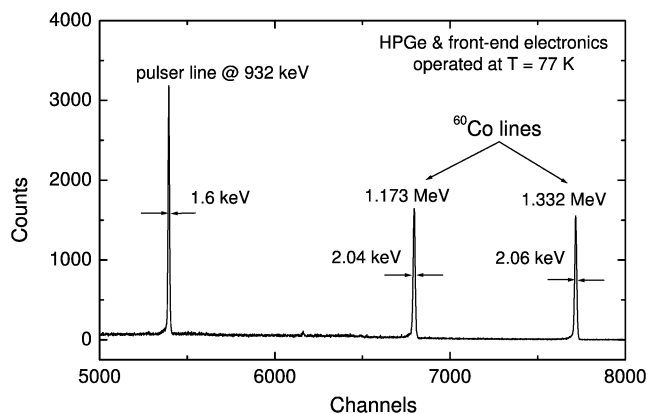


Fig. 12. Spectrum collected in the presence of a ^{60}Co source. A best resolution of 2.04 and 2.06 keV FWHM has been obtained for the two ^{60}Co lines.

In Fig. 8 the preamplifier output response to a fast input test pulse is shown, when the circuit is connected to the detector and it is driving a ~ 10 m coaxial output cable. A very clean leading edge is obtained with a transition time as fast as 16 ns. In Fig. 9 the leading edge of a signal from a ^{60}Co radioactive source is shown. Thanks to the wide preamplifier bandwidth, the shape of the leading edge is very well identified and permits application of pulse-shape analysis algorithms to localize the posi-

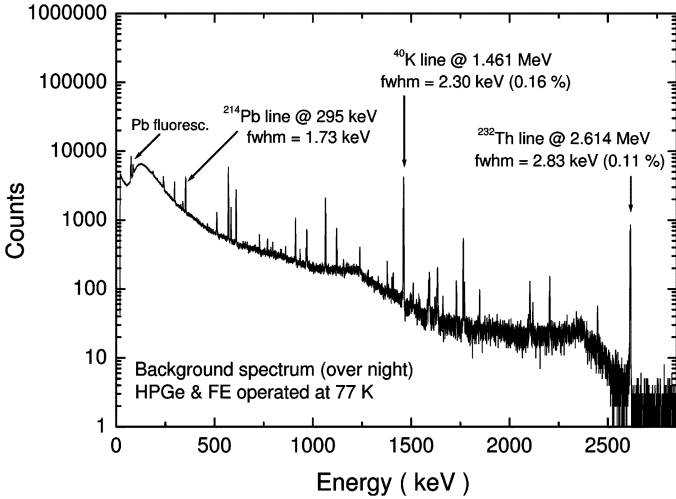


Fig. 13. Background spectrum collected over night. No peak shift or line broadening could be seen in long-term acquisitions.

tion of the photon interactions inside the crystal. In Fig. 10 the preamplifier output signal is shown in a longer time scale, when irradiating the detector with a ^{60}Co source. Note that the time constant of the baseline recovery is $\sim 250 \mu\text{s}$. The energy resolution was then tested while connecting the preamplifier output to a quasi-Gaussian shaper amplifier and a pulse height analyzer. In Fig. 11 the obtained energy resolution is shown as a function of the shaping time for a pulser line and for the 1.332 MeV ^{60}Co line. An optimum resolution of 1.6 keV FWHM is achieved for the pulser line at $6 \mu\text{s}$ shaping time. The 1.332 MeV ^{60}Co line typically shows a resolution of 2.25 keV FWHM at $6 \mu\text{s}$ and of 2.17 keV FWHM at $10 \mu\text{s}$. Note that the detector capacitance is $\sim 60 \text{ pF}$, while the input JFET capacitance is $\sim 10\text{--}15 \text{ pF}$. The choice of the BF862 model has been dictated by the need for an input device providing at the same time a very good noise performance and a very low power dissipation. Note also that the detector capacitance in the GERDA apparatus is estimated to be of $\sim 30 \text{ pF}$, so that a better capacitive matching will be finally achieved. The best collected ^{60}Co spectrum is shown in Fig. 12, where a very good resolution of 2.04 and 2.06 keV FWHM has been achieved for the ^{60}Co lines. In Fig. 13 a background spectrum collected over night is shown. A resolution of 1.7 keV FWHM or better is measured for the lines at the lowest energies. The limit in the achieved resolution corresponds to the electronic noise contribution. This is to be regarded as a good performance considering that the value for the detector capacitance is relatively high and that the naked preamplifier is working as directly immersed in liquid nitrogen, which is a very unusual operating condition. No peak shift or line broadening could be seen in this long-term acquisition, and very high resolutions, of the order of 0.1%, could be achieved and maintained for the highest energy lines. A resolution of 2.30 keV FWHM (0.16%) has been measured for the background line of ^{40}K at 1.461 MeV. A resolution of 2.83 keV FWHM (0.11%) has been achieved for the background line of ^{232}Th at 2.614 MeV. The excellent stability of the line positions in long-term acquisitions confirms the very high loop gain of the preamplifier. A very good gain stability has also been observed when

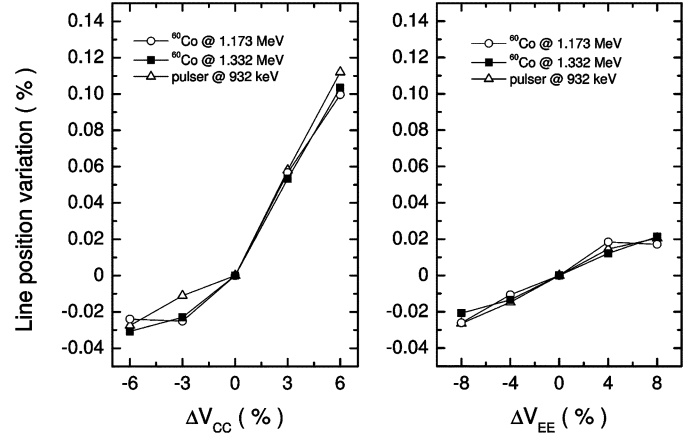


Fig. 14. Percent variation of the centroid positions of the spectrum lines as a function of the percent variation of the positive (V_{CC}) and negative (V_{EE}) power supply voltages of the ASIC.

TABLE II
PREAMPLIFIER SPECIFICATIONS

Working temperature	from -196°C to 55°C (from 77 K to 328 K)
Negative output voltage swing on 150Ω impedance	$\sim 2.5 \text{ V}$ (against a negative power supply of -2.7V)
Energy sensitivity ($C_F = 0.2 \text{ pF}$)	$\sim 290 \text{ mV/MeV}$ at preamp output $\sim 217 \text{ mV/MeV}$ after 150Ω termination
Input dynamic range	$\sim 8.6 \text{ MeV}$
Rise time at $T = 77\text{K}$	$\sim 16 \text{ ns}$ with $\sim 10\text{m}$ terminated coaxial cable
Fall time at $T = 77\text{K}$	$\sim 250 \mu\text{s}$ ($R_F = 1.2 \text{ G}\Omega$)
Open-loop gain at $T = 300\text{K}$	$\sim 3.5 \cdot 10^5$ (Spice simulation)
Loop gain at $T = 300\text{K}$	~ 600 (Spice simulation)
Resolution at $T = 77 \text{ K}$ ($\tau = 6 \mu\text{s}$)	2.2 keV @ 1.332 MeV (^{60}Co) 1.6 keV @ 932 keV pulser line
Power required at $T = 77\text{K}$	23.4 mW ($V_{FET} = +4\text{V}$ $I_D = 3\text{mA}$ $V_{CC} = +3.6\text{V}$ $V_{EE} = -2.8\text{V}$)

measuring the line-centroid positions versus the value of the power supply voltages. In particular, as shown in Fig. 14, the rejection to changes in positive and negative power supply voltages of the ASIC circuit is shown. The position of the ^{60}Co lines, as well as that of the pulser line, show a typical stability of better than 0.1% against much larger percent variations, of up to $\pm 8\%$, in the power supply voltages.

The preamplifier specifications are summarized in Table II. The negative output voltage swing is $\sim 2.5 \text{ V}$, with a negative power supply of -2.7 V . As the preamplifier sensitivity is $\sim 290 \text{ mV/MeV}$ (not terminated), the input dynamic energy range is $\sim 8.6 \text{ MeV}$. The quoted open-loop and loop gains have been obtained by a computer simulation at 300 K. The available transistor Spice models have in fact a temperature range validity of -40°C up to 125°C , so that running a simulation at

−196°C (77 K) is not allowed. The measured power consumption of the preamplifier is only 23.4 mW. This is particularly important for future multi-channel arrays of germanium detectors, where a relatively high count of read-out channels inside the cryogenic liquid is foreseen.

V. CONCLUSIONS

A low-noise JFET-CMOS charge preamplifier for HPGe detectors has been realized and tested in the framework of the GERDA experiment. The circuit is functional both at room and cryogenic temperature and meets the requirements of high-resolution gamma-ray spectroscopy with germanium detectors. It has been tested in conjunction with an unsegmented p-type HPGe detector and operated inside a liquid nitrogen dewar at 77 K. The detector capacitance is ~ 60 pF. An optimum resolution of 1.6 keV FWHM has been obtained on the pulser line at 6 μ s shaping time. The 1.332 MeV line from a ^{60}Co source shows a typical resolution of 2.2 keV FWHM. A wide bandwidth (rise time of ~ 16 ns) and a low power consumption (~ 23 mW) make the preamplifier suitable for multi-channel arrays of germanium detectors, where pulse-shape analysis techniques may be applied in order to localize the interaction positions of the gamma-photons inside the crystals.

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